

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	12	(fpga same microcontroller same lock\$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L8	0	(fpga same dut same lock\$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L12	699	(target adj (device microcontroller)) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L16	0	(microcontroller and fpga and pod) and (co\$simulation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L17	1	(microcontroller and fpga and pod) and (co\$verification)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L18	1	(microcontroller and pod) and (co\$verification)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L19	0	(microcontroller and pod) and (co\$simulation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L23	990	(fpga same access) and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L25	376	(fpga same access same microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47

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L32	494	(fpga same (access visibility) same (dut microcontroller target))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L33	11	L32 and pod	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L34	80	L32 and emulat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L36	494	(fpga same (access visibility) same (dut microcontroller target))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L37	80	L36 and emulat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L41	376	(fpga same access same microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47
L47	376	(fpga same access same microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/31 17:47